

02/21/02  
JC930 U.S. PTO

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February 21, 2002

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Attorney Docket No.: 10559/587001/P12768

**Box Patent Application**  
Commissioner for Patents  
Washington, DC 20231

Presented for filing is a new original patent application of:

Applicant: BEEN-YIH JIN, REZA ARGHAVANI AND ROBERT CHAU

Title: NON-SILICON SEMICONDUCTOR AND HIGH-K GATE  
DIELECTRIC METAL OXIDE SEMICONDUCTOR FIELD EFFECT  
TRANSISTORS

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):

FR  
BOSTON  
DALLAS  
DELAWARE  
NEW YORK  
SAN DIEGO  
SILICON VALLEY  
TWIN CITIES  
WASHINGTON, DC

	<u>Pages</u>
Specification	16
Claims	6
Abstract	1
Declaration	[To be Filed at a Later Date]
Drawing(s)	5

Enclosures:

— Postcard.

There are 21 total claims, 3 of which are independent.

CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. EV044492533US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit

February 21, 2002

Signature

Gabriel Lewis

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Page 2

Basic filing fee	\$0
Total claims in excess of 20 times \$18	\$0
Independent claims in excess of 3 times \$84	\$0
Fee for multiple dependent claims	\$0
Total filing fee:	\$0

No filing fee is being paid at this time.

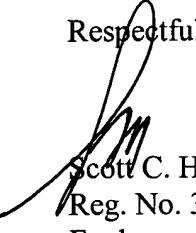
If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (858) 678-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please send all correspondence to:

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Respectfully submitted,

  
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Reg. No. 32,030  
Enclosures  
SCH/rpi  
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